

GPU ARCHITECTURE & GEOMETRY SHADER

Luca Del Bolgia

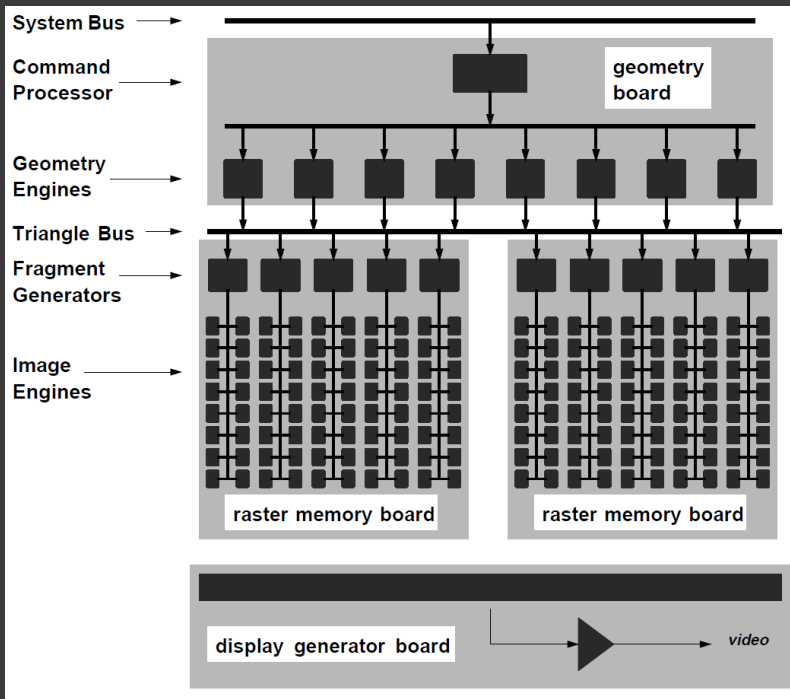
Summary

- ⦿ What we know - CG course
- ⦿ Changes to the pipeline
- ⦿ Geometry Shader
- ⦿ DX11

WHAT WE KNOW SUMMARY

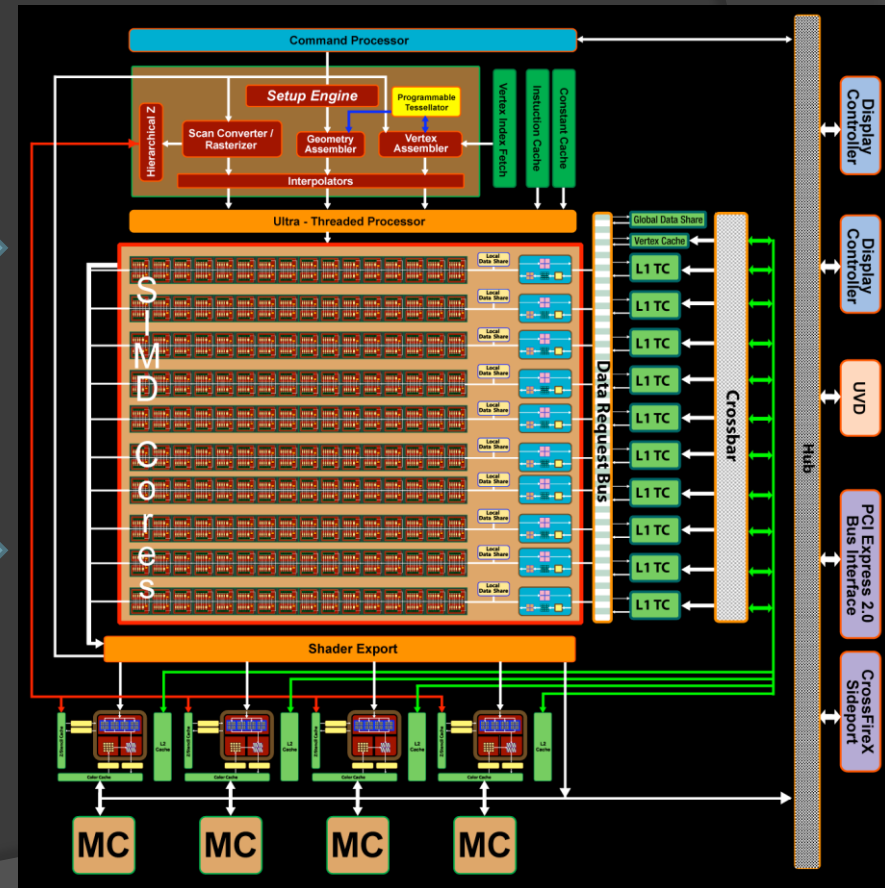
SUPERCOMPUTERS

SGI Reality Engine

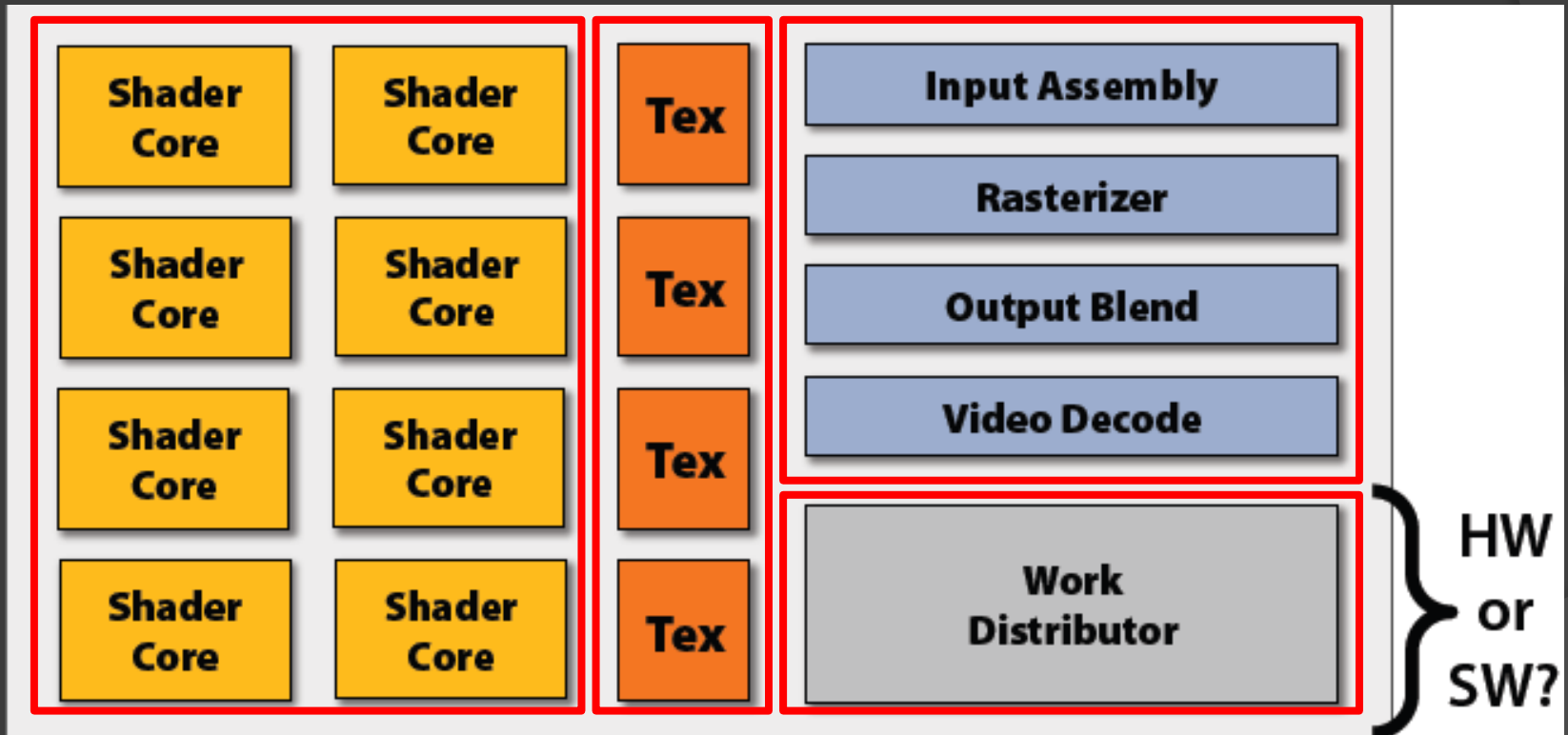


GPU

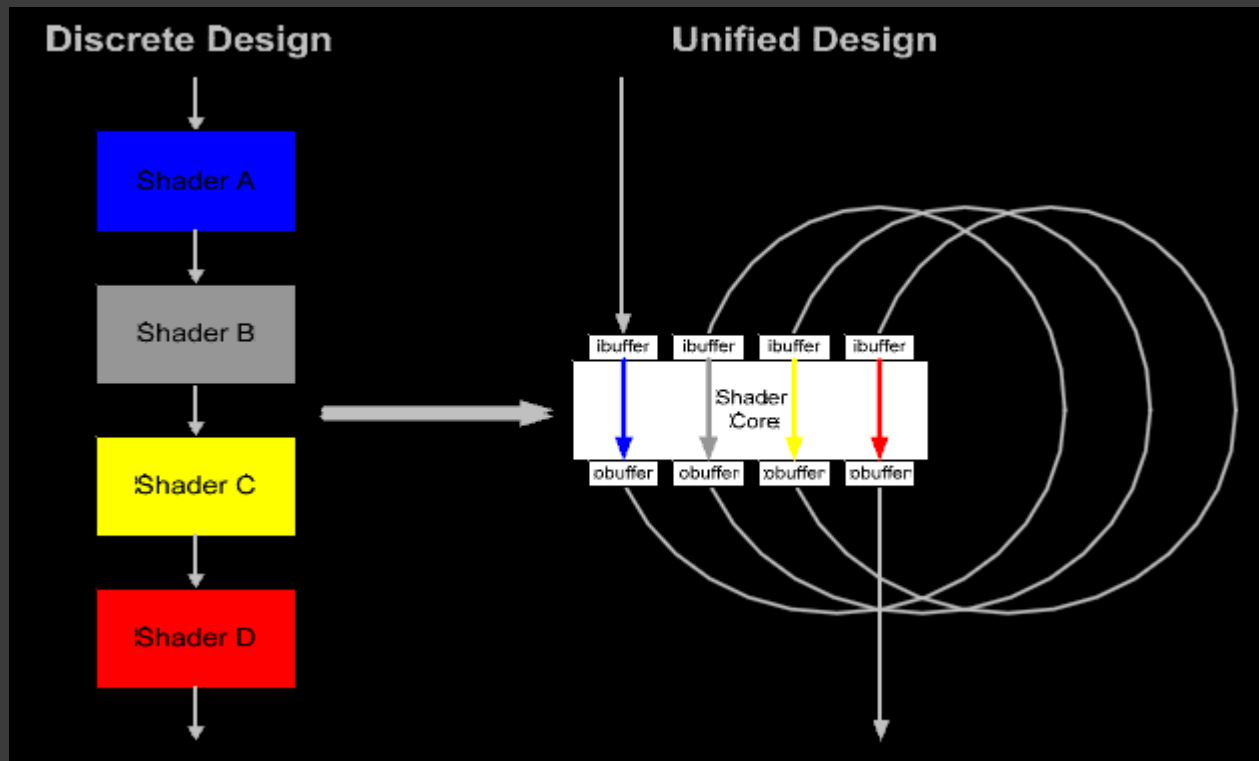
ATI RV770



Inside the GPU



Unified Shader



Unified Shader

Vertex Shader



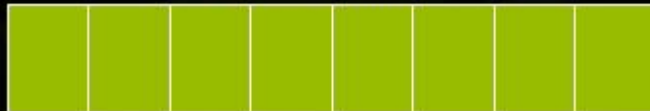
Pixel Shader



Vertex Shader



Pixel Shader



Heavy Geometry
Workload Perf = 4



Heavy Pixel
Workload Perf = 8

Unified Shader

Unified Shader



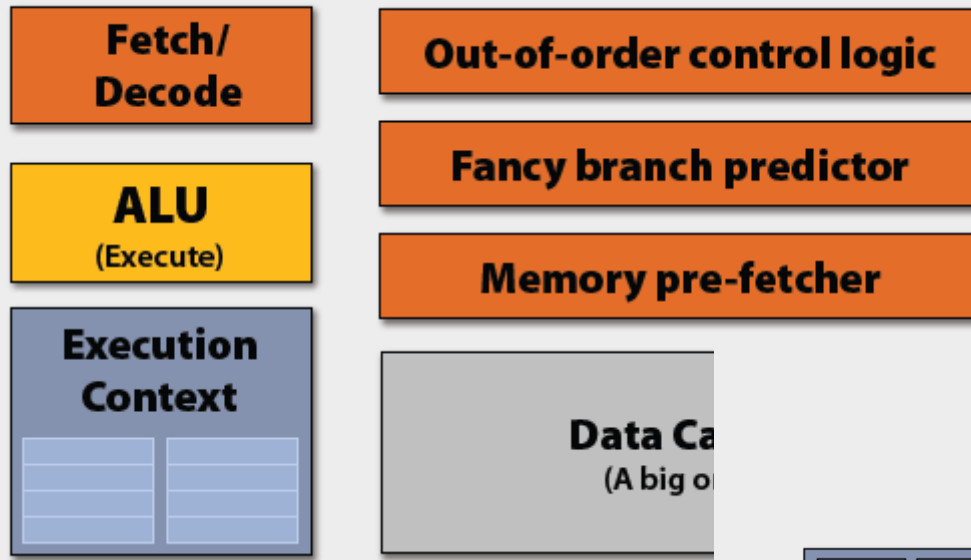
Heavy Geometry
Workload Perf = 11

Unified Shader

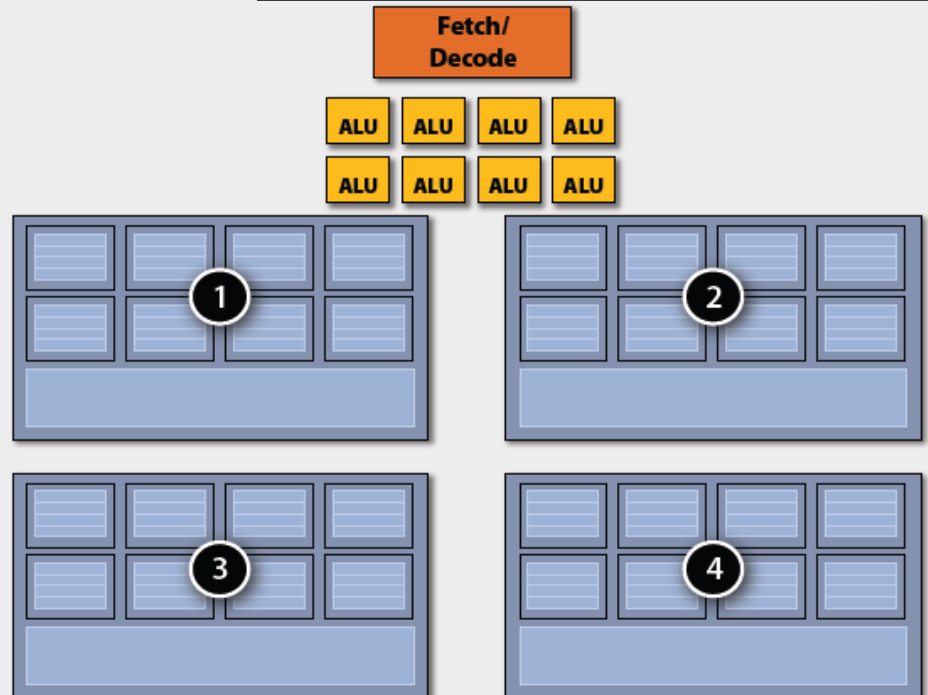


Heavy Pixel
Workload Perf = 11

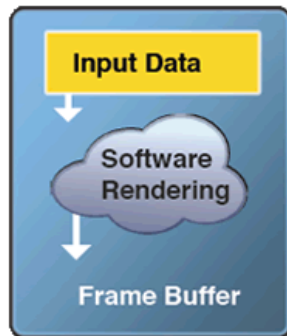
From CPU to GPU



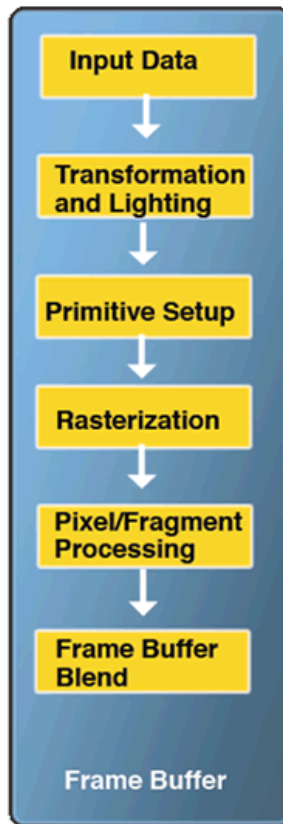
Three Key Ideas



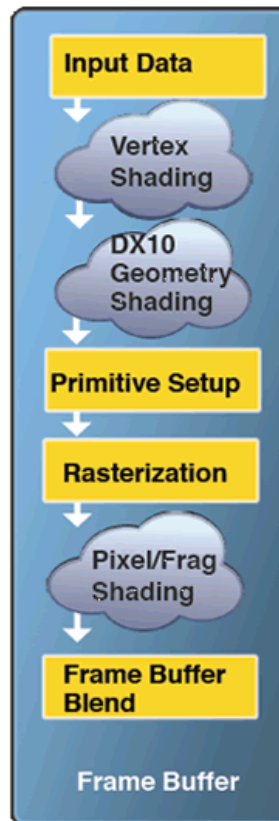
Pipeline



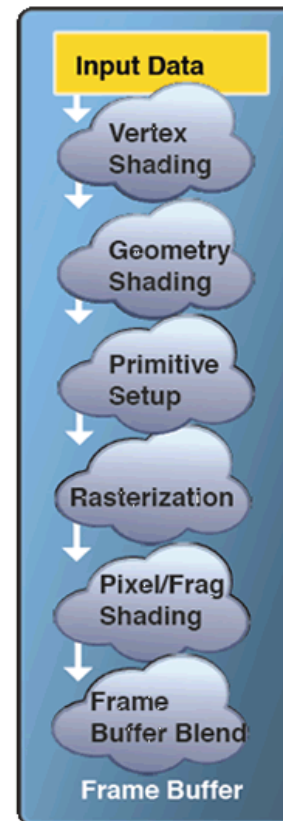
PRE 1996
CUSTOMIZED
SOFTWARE
RENDERING



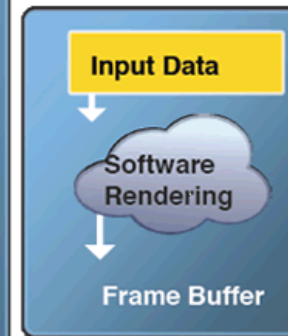
PRE 2001



DX10

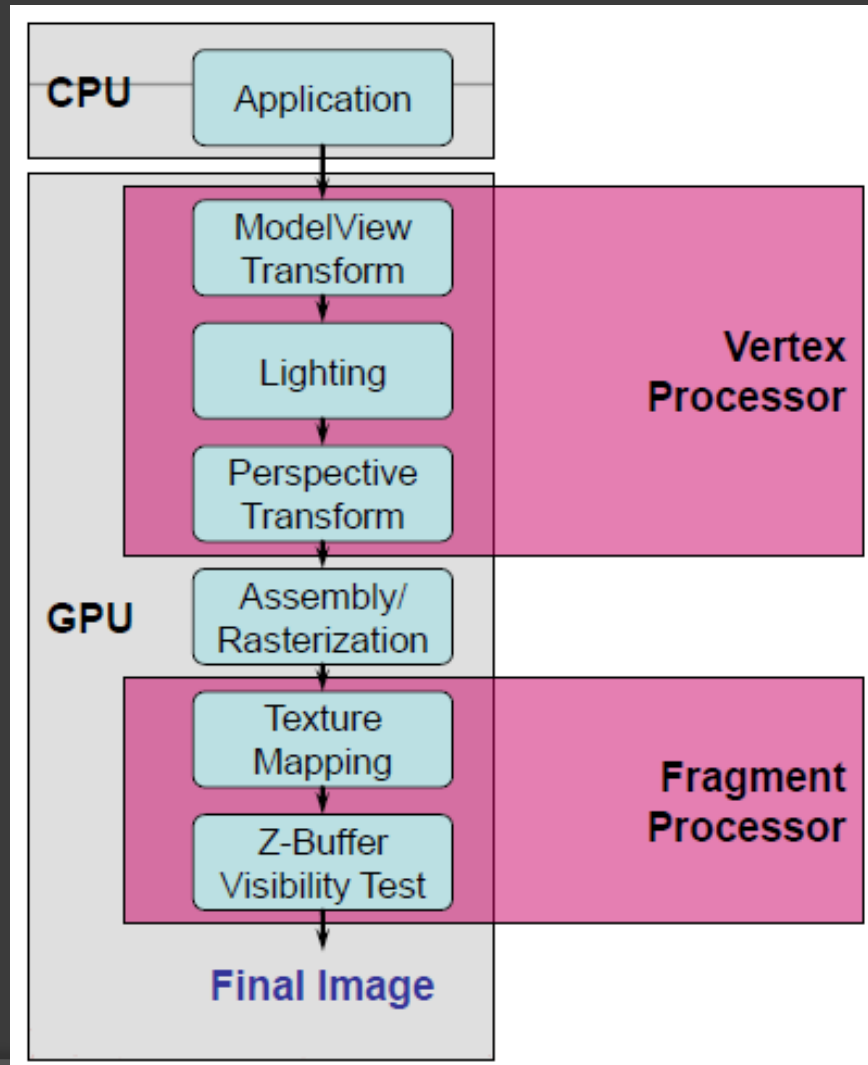


NO FIXED
FUNCTION?



SOFTWARE
RENDERING?

Vertex and Fragment

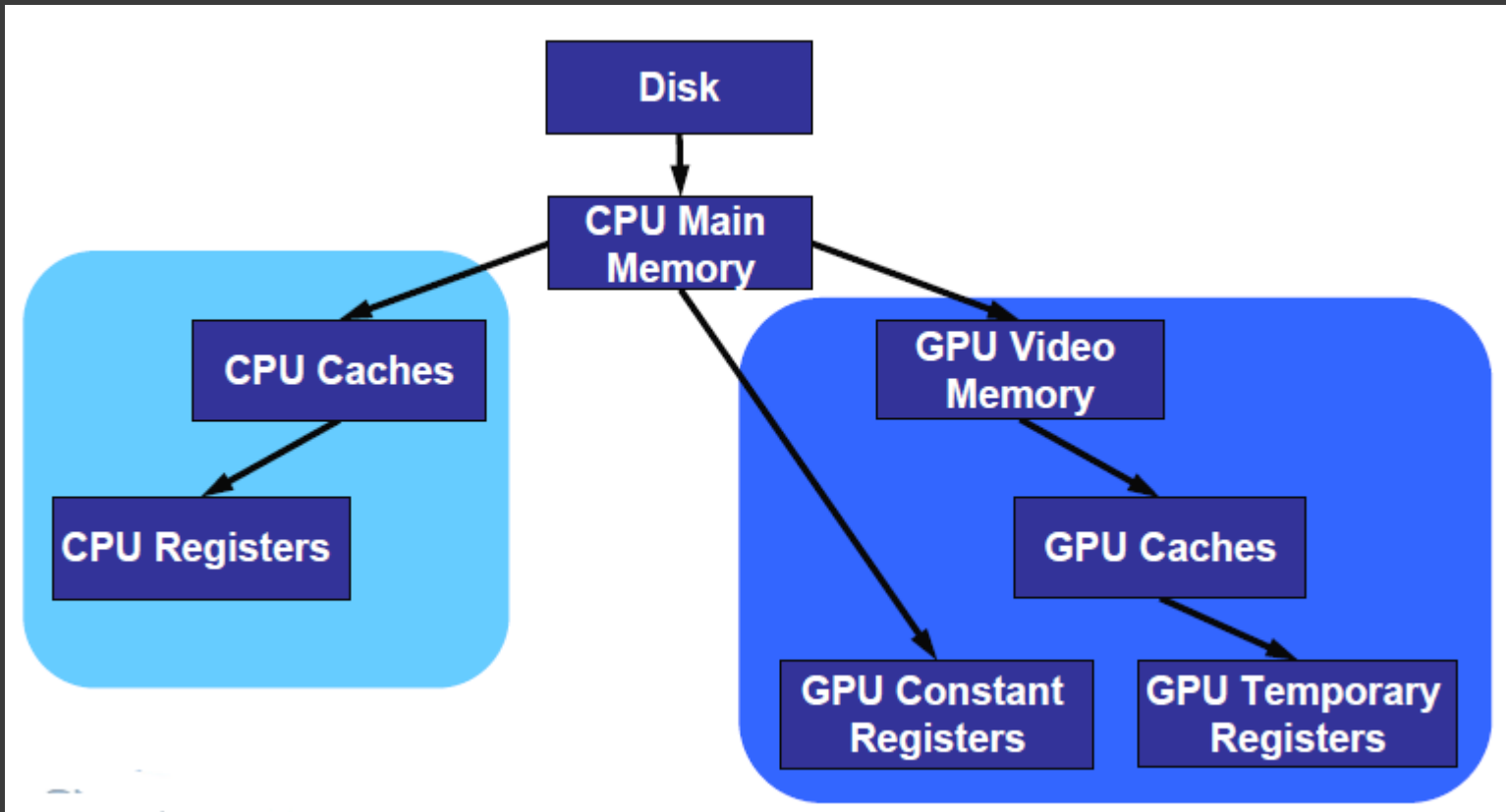


Where?

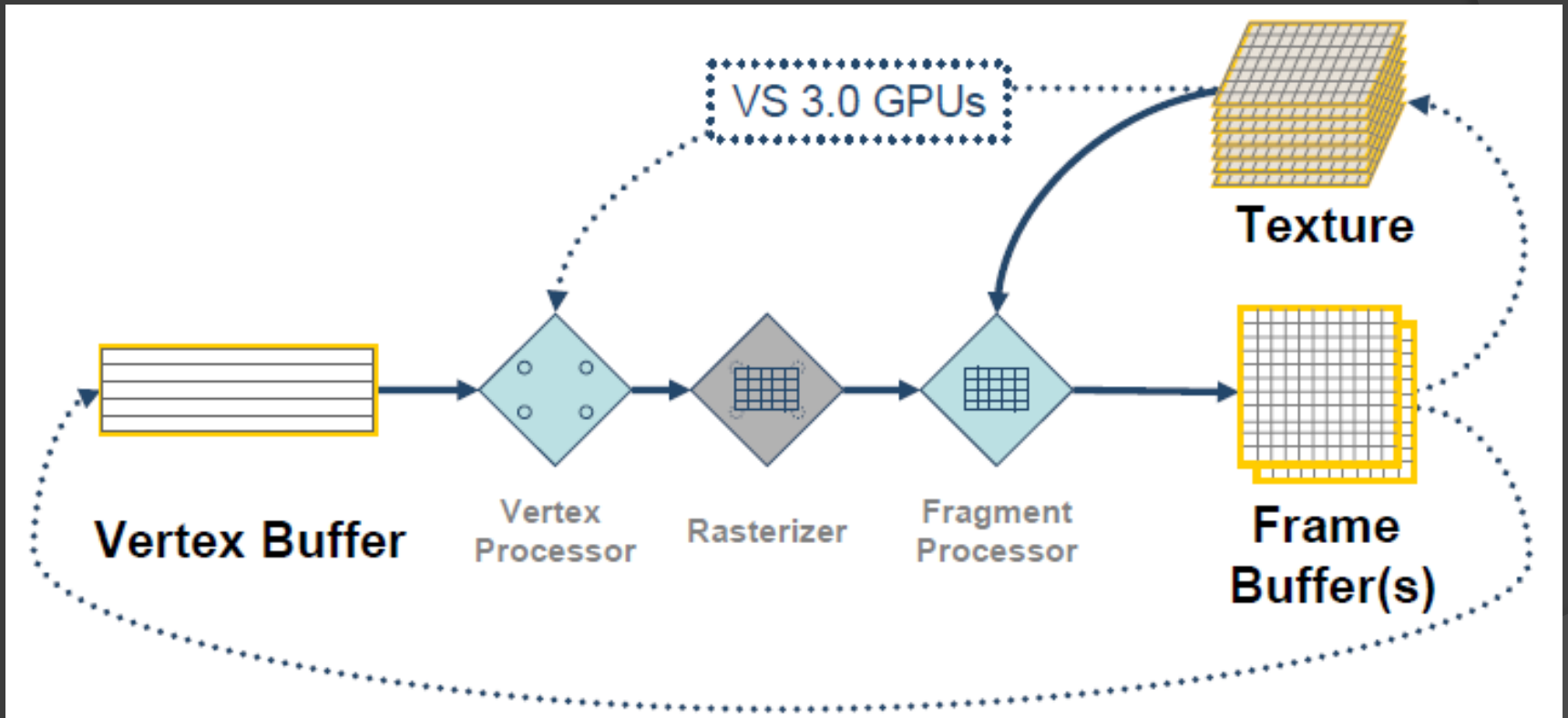
Geometry
Processor

Why?

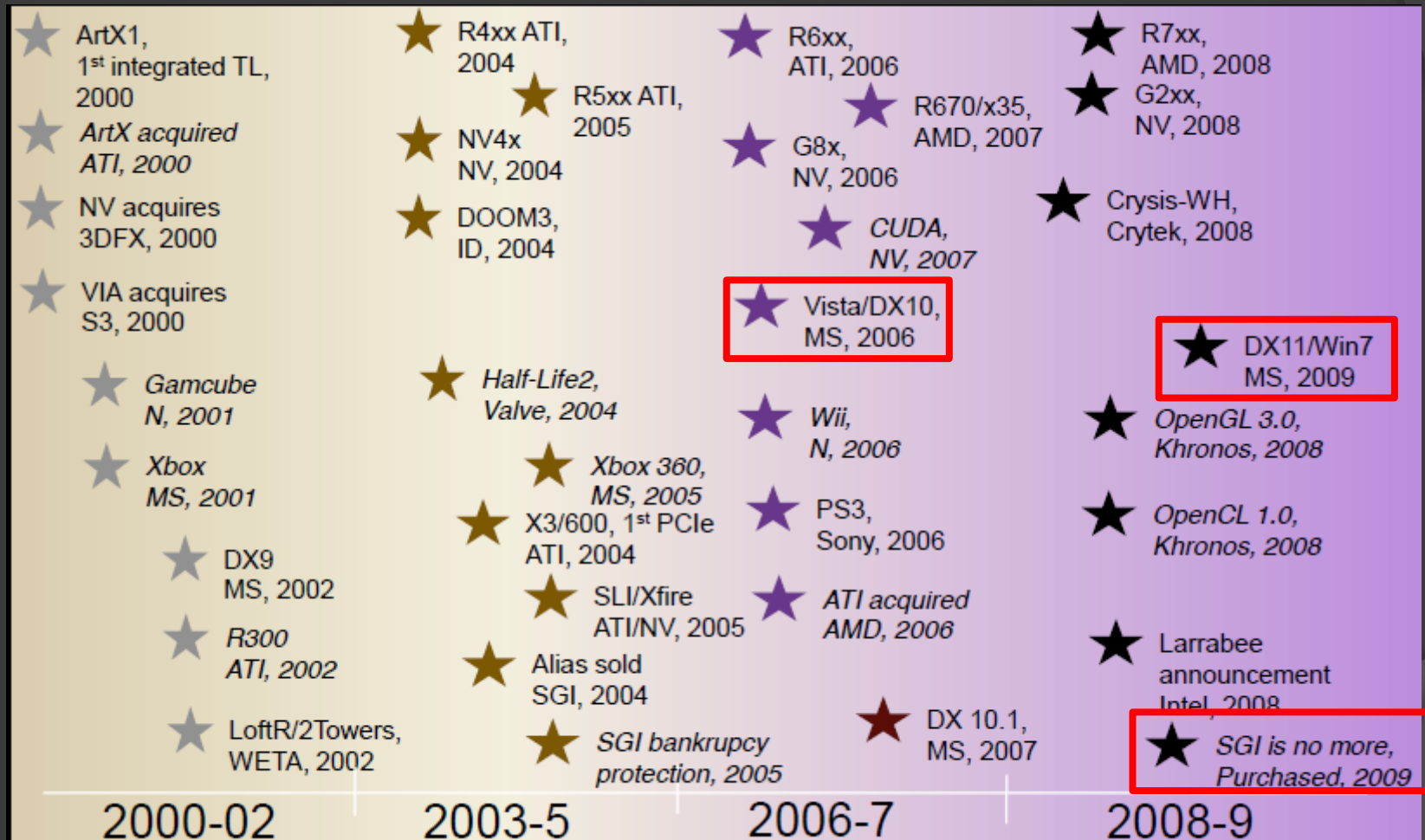
Memory Model



Memory Model

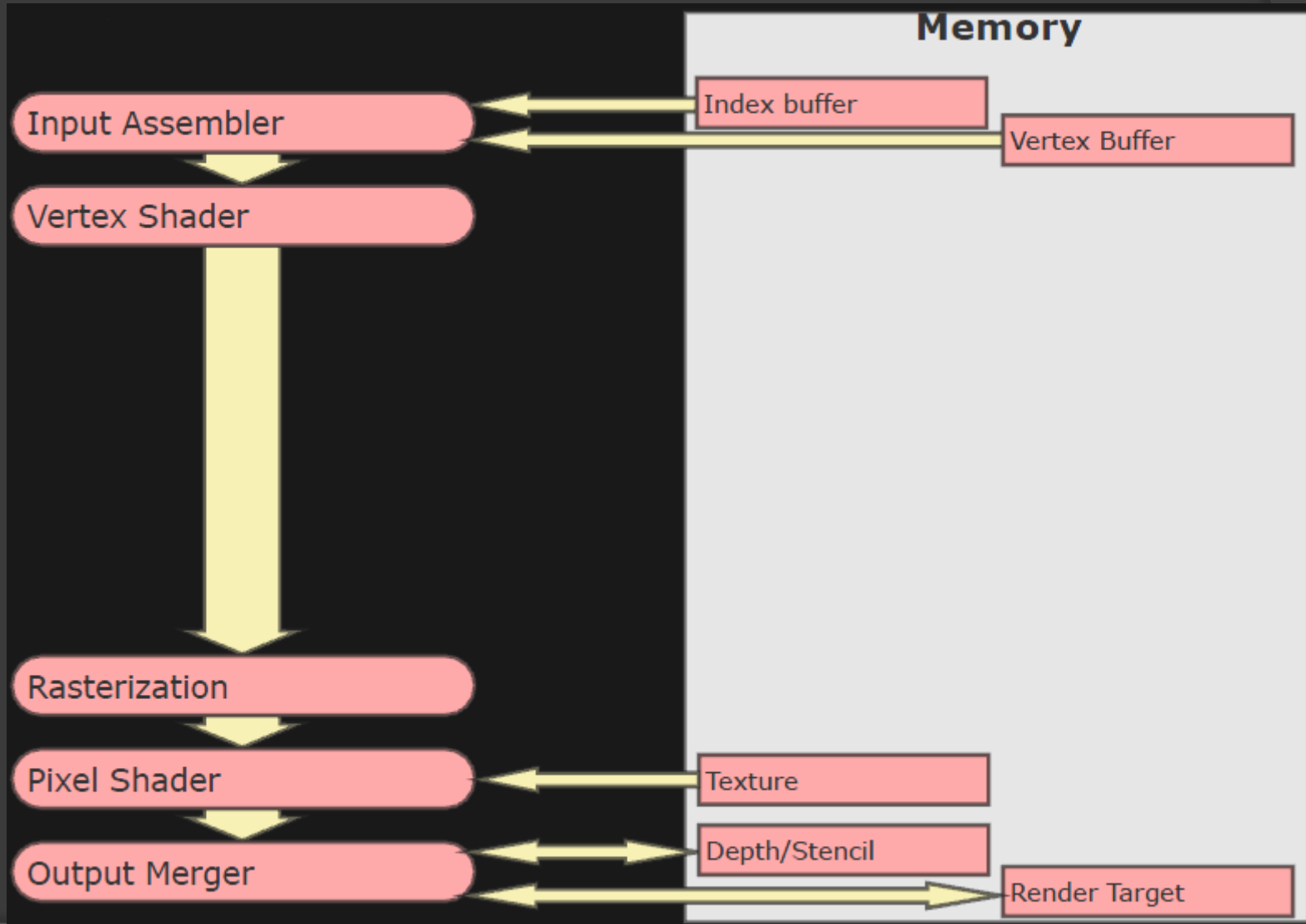


A quick history since 2000

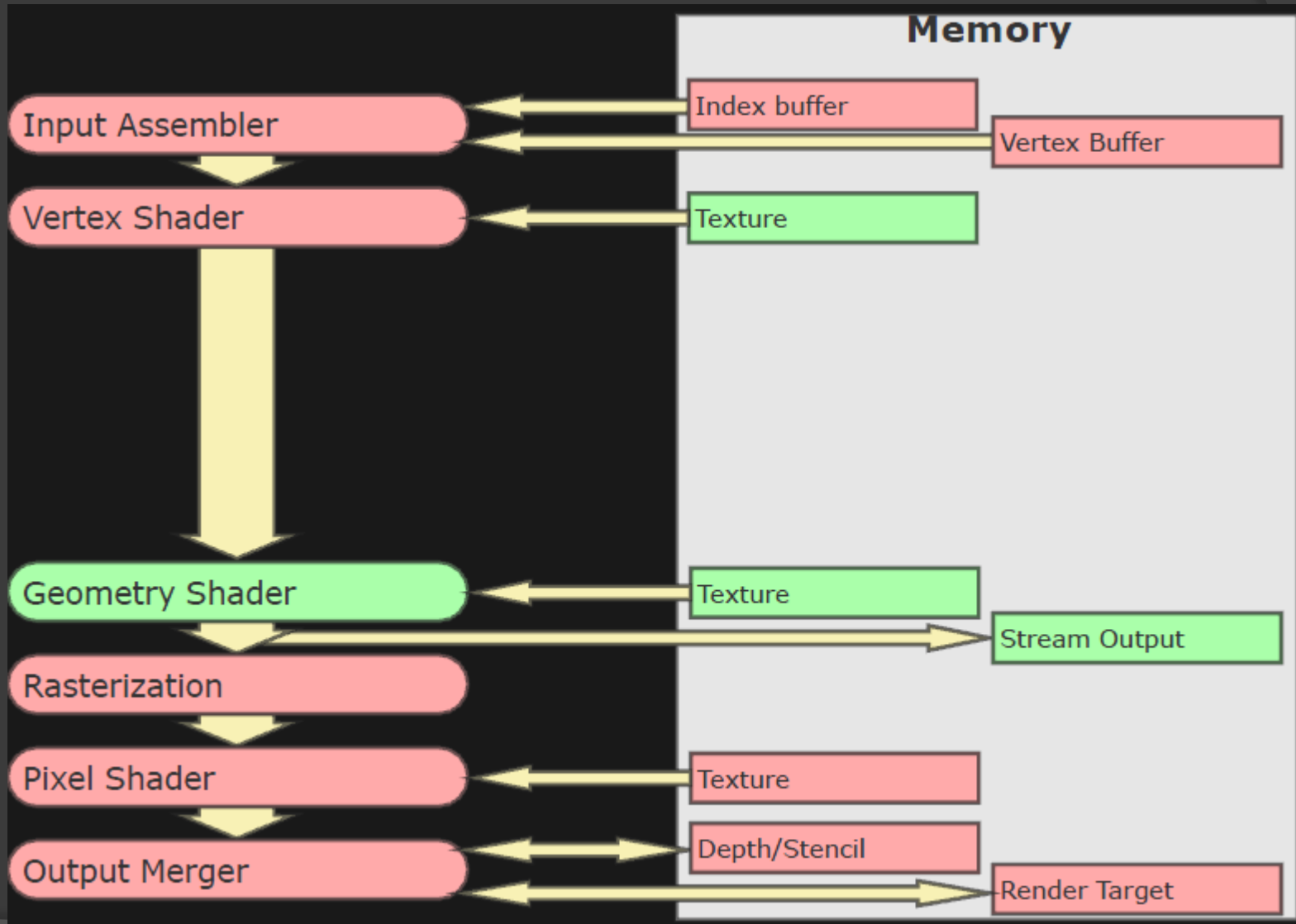


CHANGES TO THE PIPELINE

DX 9



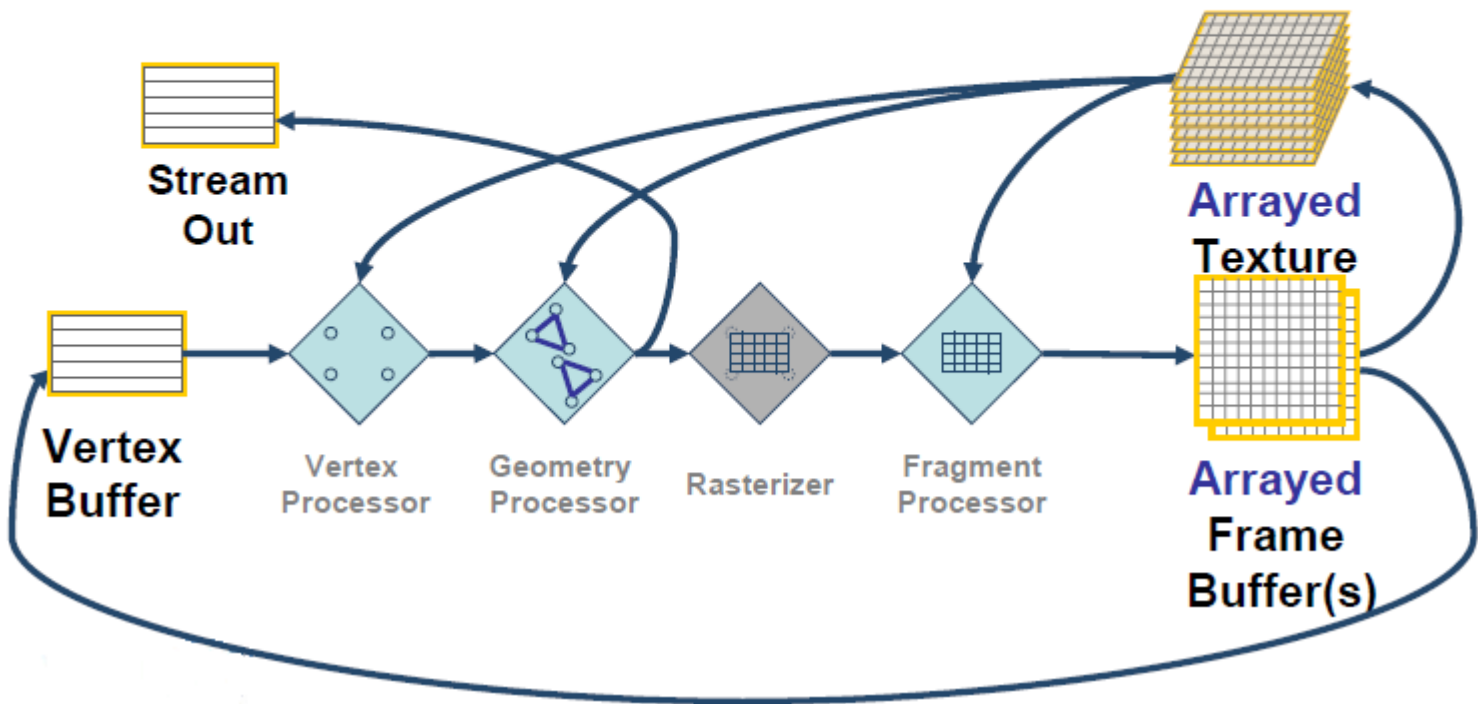
DX 10



DX10 – Memory model

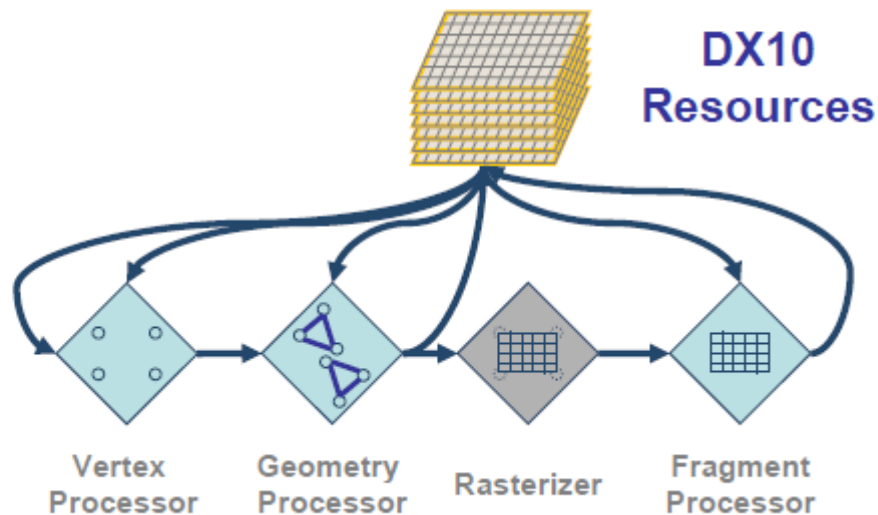
- **More flexible memory handling**

- All programmable units can read texture
- “Stream out” after geometry processor



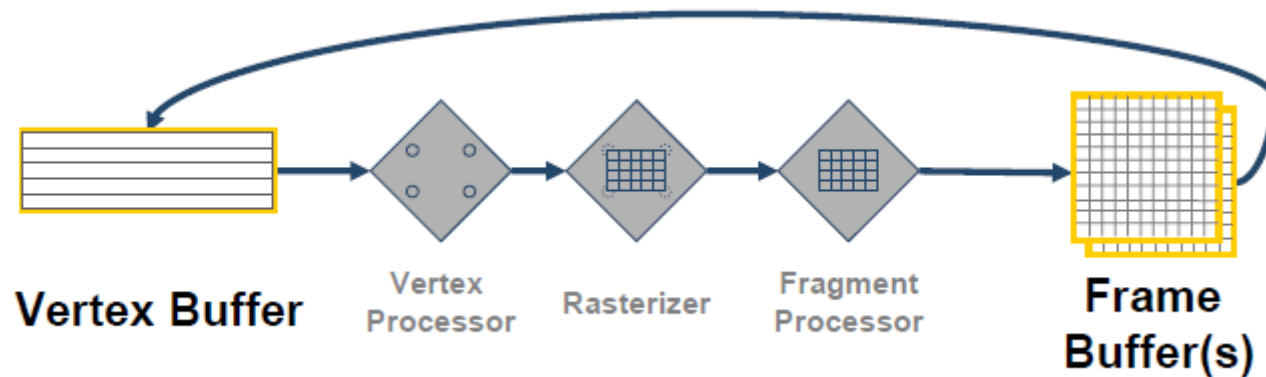
DX10 – Memory model

- DX10 provides “resources”
- Resources are flexible!



DX10 – Memory model

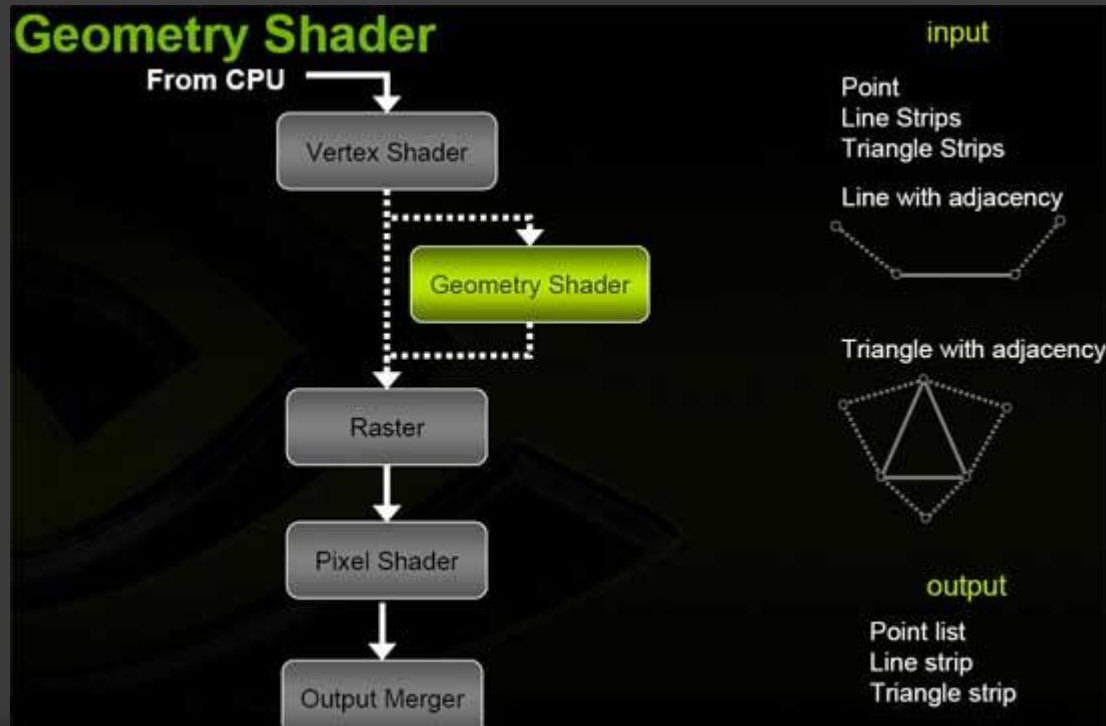
- Enables top-of-pipe feedback loop
- Enables dynamic creation of geometry on GPU



GEOMETRY SHADER

Geometry Shader

- Input
 - Vertices of a *fullprimitive* (1 for point, 2 for line, 3 for triangle)
 - Vertices of *edge-adjacentprimitives*
- Output
 - Point-, line- or triangle-strip
 - Limited output size

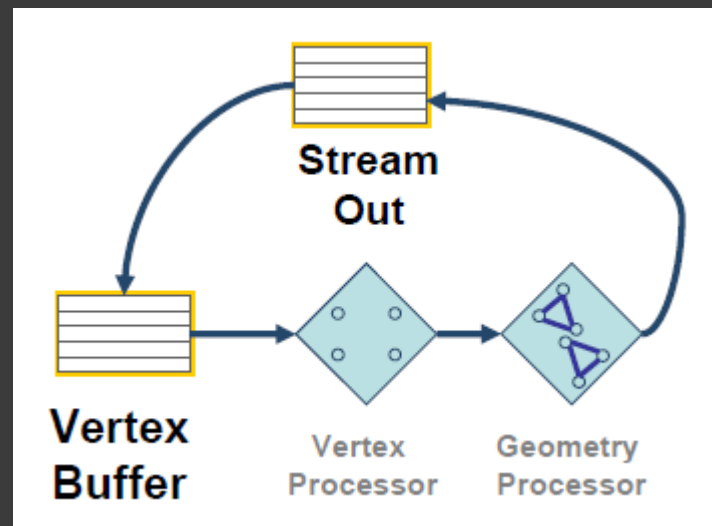
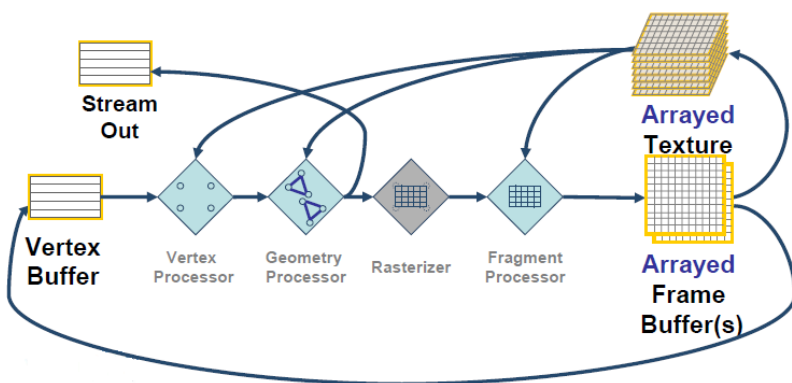


Stream Out to Vertex Buffer

- Enabled by DX10 StreamOut capability
- Expected to be used for dynamic geometry
 - Geometry processor(shader) produces 0-n outputs per input

- **More flexible memory handling**

- All programmable units can read texture
- “Stream out” after geometry processor

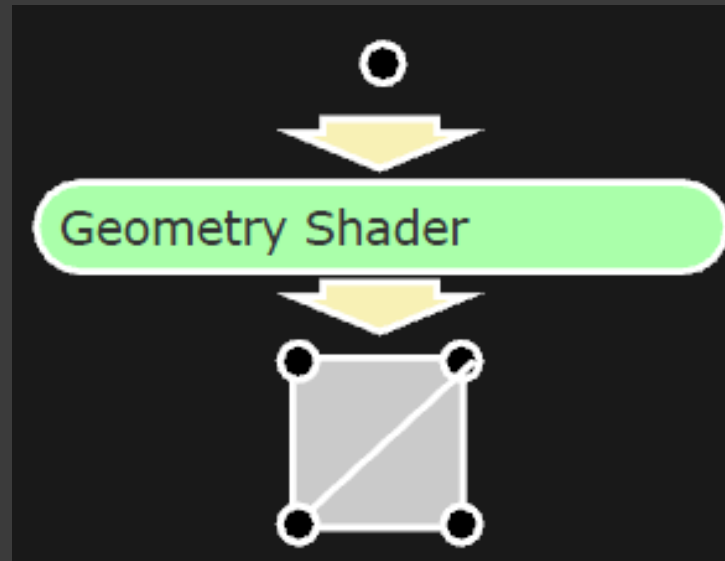


Usage examples

- ⦿ Tessellation;
- ⦿ Point Sprite Tessellation;
- ⦿ Wide Line Tessellation;
- ⦿ High Performance Culling;

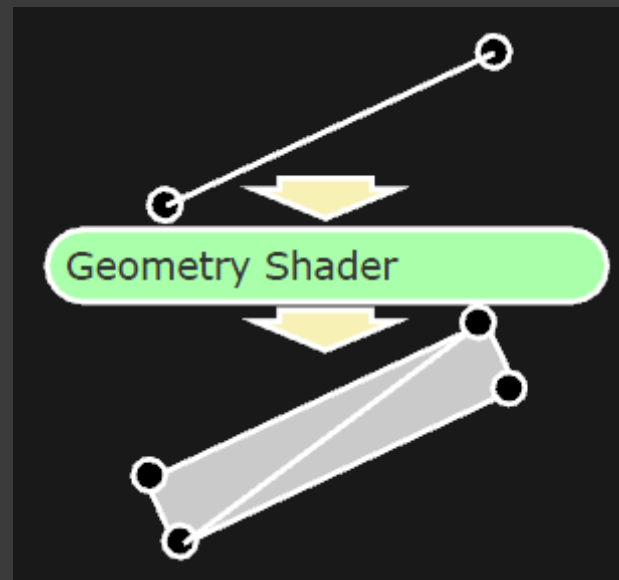
Point Sprite Tessellation

- The shader takes in a single vertex and generates four vertexes (two output triangles) that represent the four corners of a quad



Wide Line Tessellation

- The shader receives two line vertexes and generates four vertexes for a quad that represents a widened line.



Examples

- ⦿ Smoke represented by point sprites
 - Points converted to quads in geometry shader
- ⦿ Hair represented by “wide lines”
 - Lines converted to quads in geometry shader



High Performance Culling

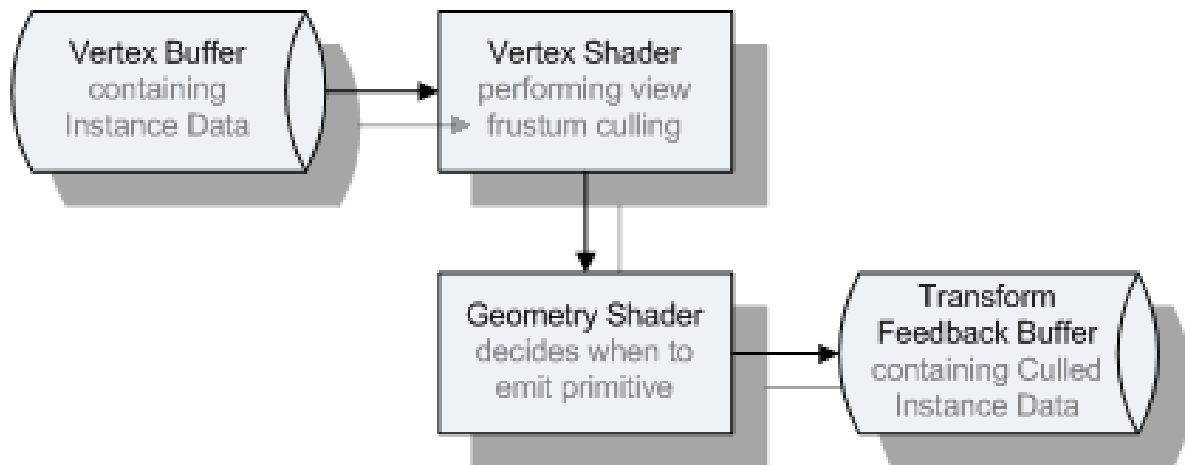
- If we take a closer look at the geometry shader we observe that the most revolutionary in it is not that it can raise the number of emitted primitives but that it can discard them.

Instance Cloud Reduction

- It is a multi-pass technique that in the first pass culls the object instances against the view frustum using the GPU and in the second pass renders only those instances that are likely to be visible in the final scene.
- it does not produce any fragments in the first pass, instead the first pass does the view frustum culling.

Pass 1: Culling

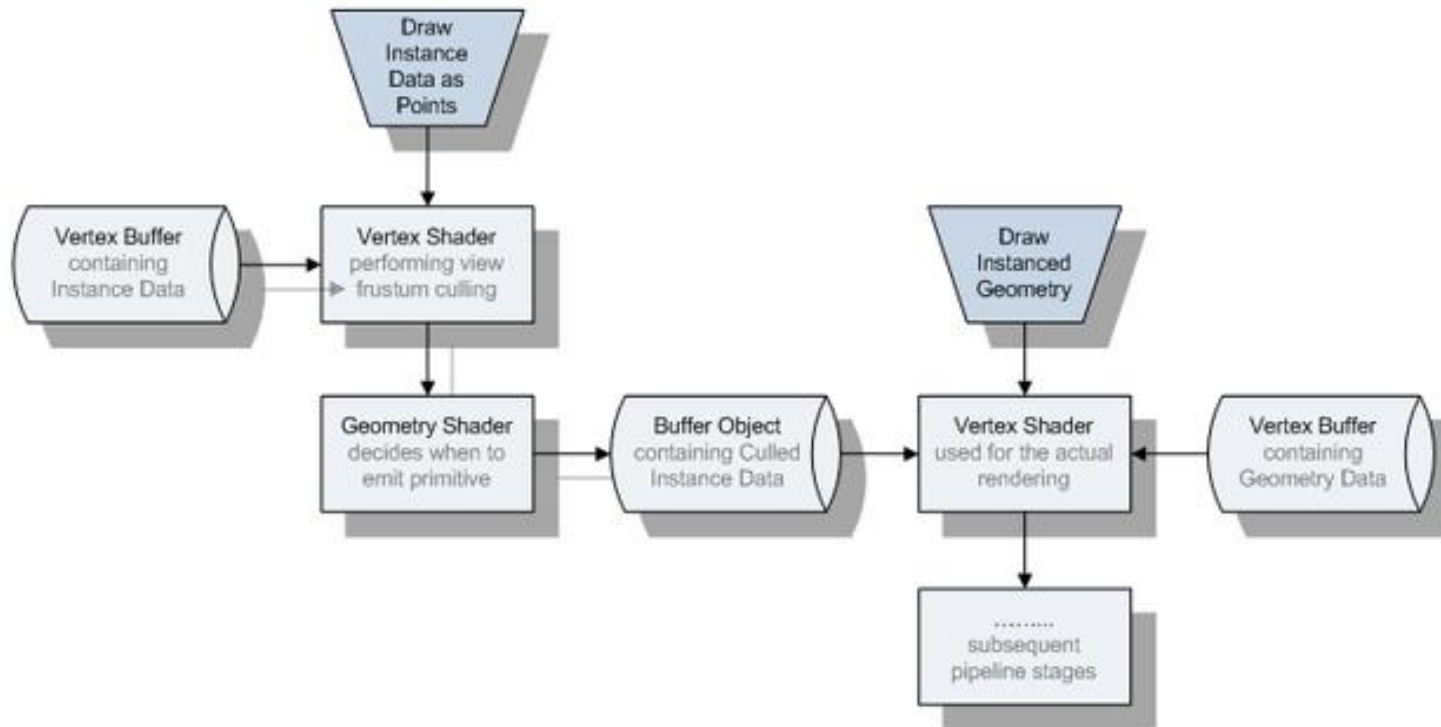
- In the first pass the vertex shader determines whether the actual object instance's bounding volume is inside the view frustum and sends a flag about the culling to the geometry shader.
- The geometry shader will emit the instance data to the destination buffer if the flag says that the instance is likely to be visible or does not emit anything if it is determined that the object instance is out of view.



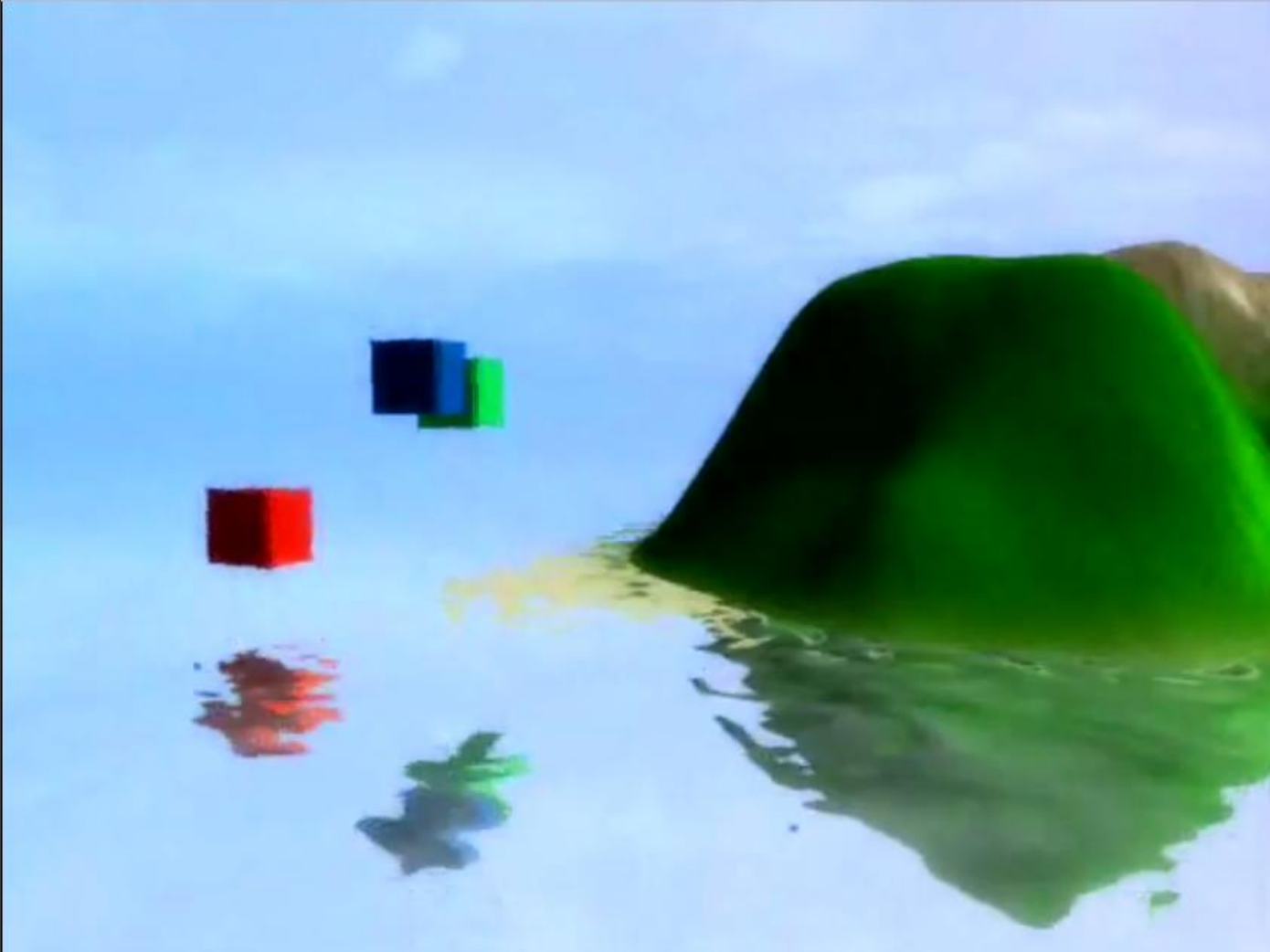
Pass 2: Rendering

- In the second pass there is nothing special to do. The only things that need to be changed is that the instance data for the rendering must be sourced from the generated culled instance data buffer.

Pass 1 & 2

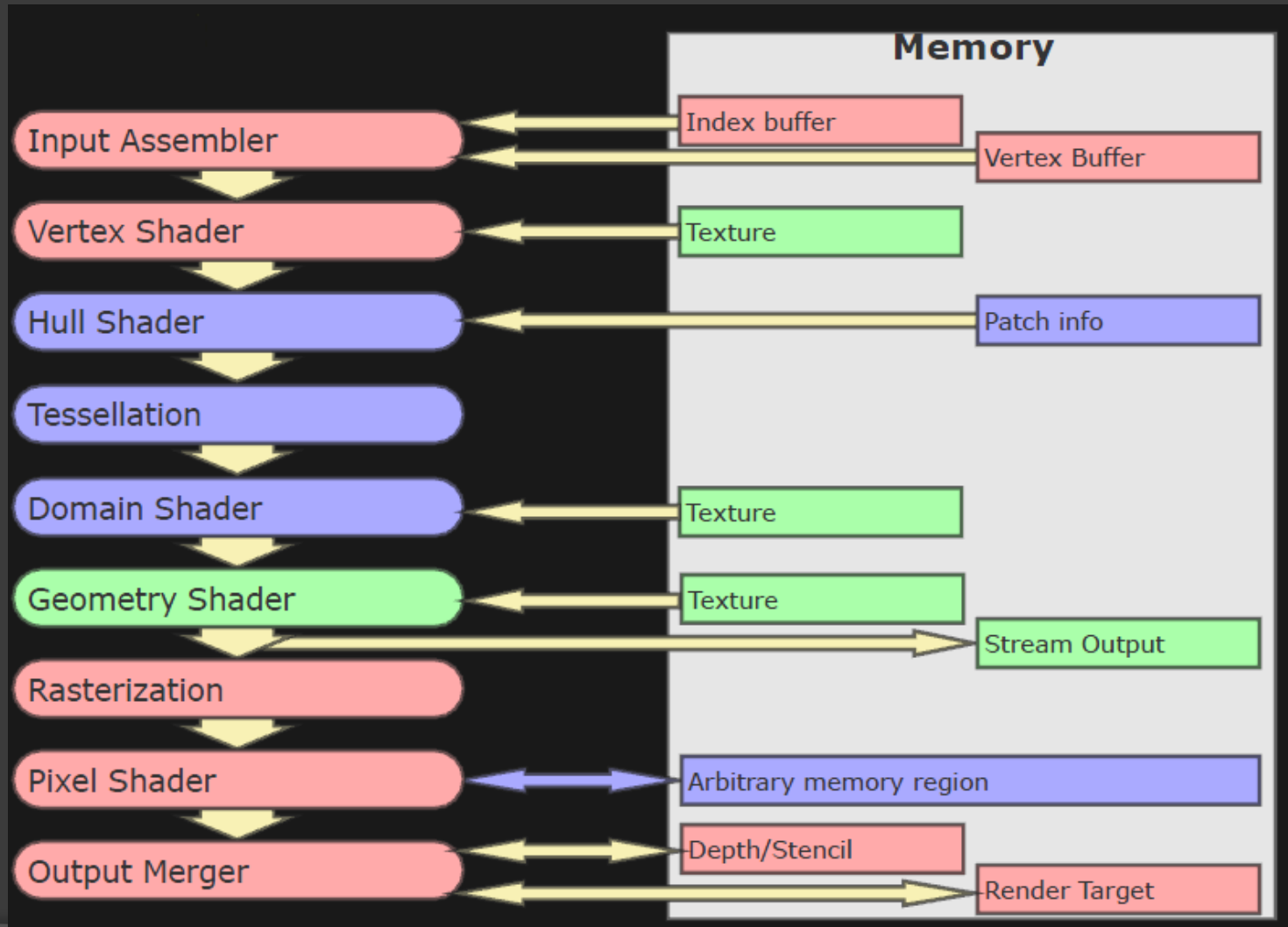


TRIANGLE SHRINK



DX11

DX11



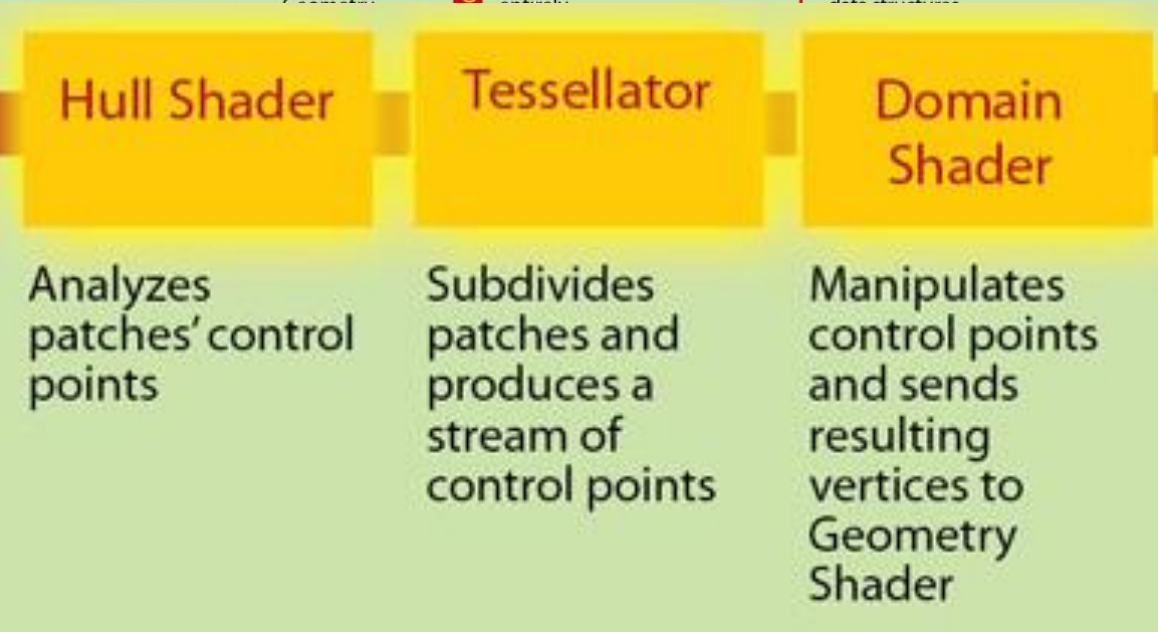
DX11

HOW IT WORKS

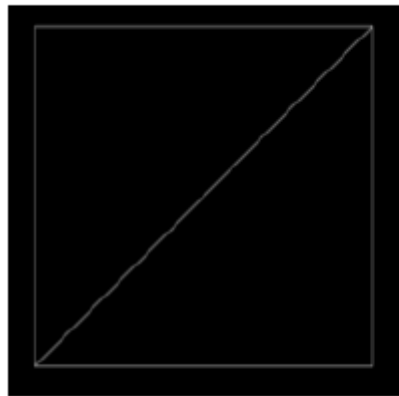
Shader Model 5.0 Pipeline



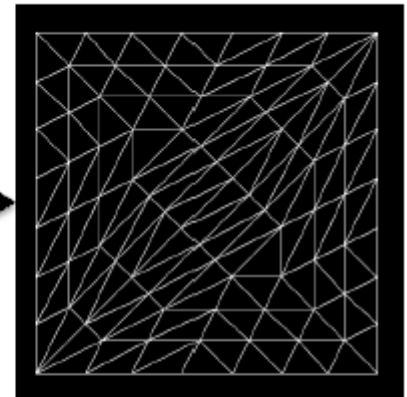
Shader Model 5.0 adds three new stages to the pipeline in order to perform tessellation. The Hull Shader will be a boon to the general-purpose pipeline which aims to harness the parallel processing power of modern graphics processors have in such



Tessellation

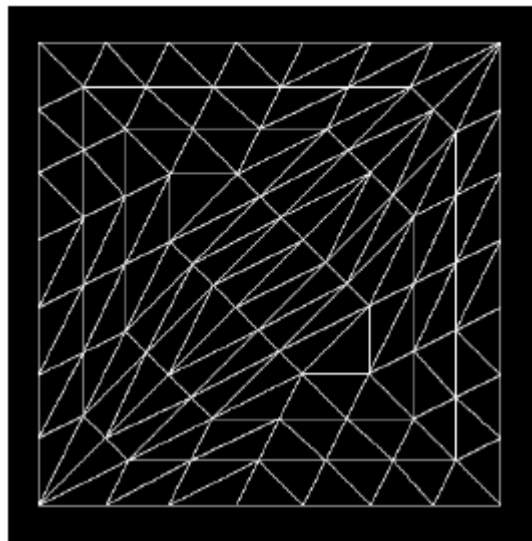


Triangle
Patch Mesh

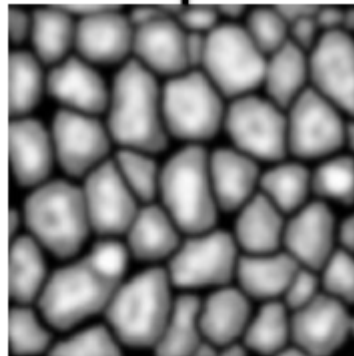


Tessellated
Mesh

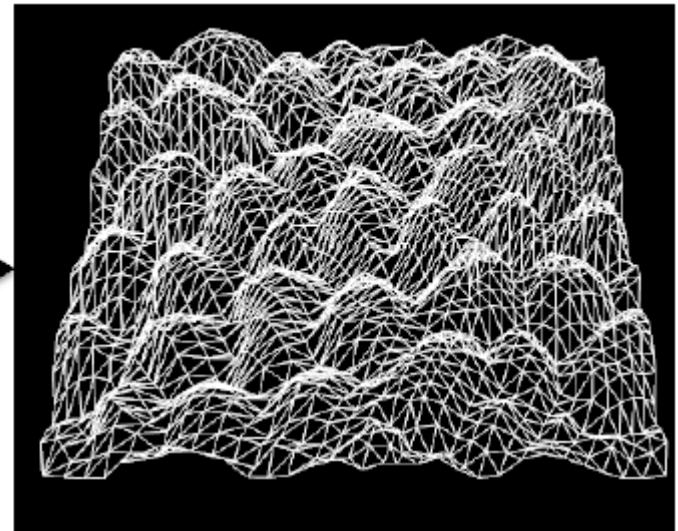
Tessellation



Tessellated
Mesh



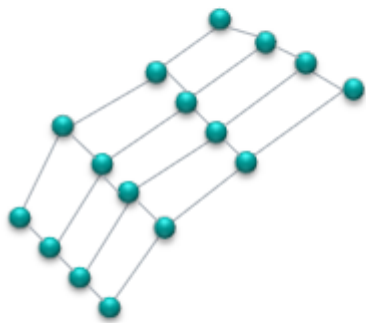
Displacement Map



Displaced
Mesh

Bezier Patch

Bicubic Bezier Patch Example



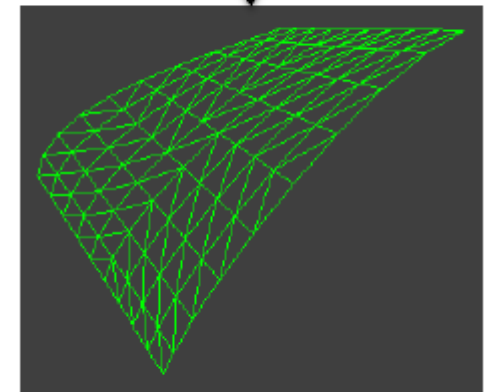
Bezier Control Points



$$\text{Pos}(u,v) = \sum_{m=0}^3 \sum_{n=0}^3 B_m(u) B_n(v) G$$

Where: $B_0 = (1-t)^3$
 $B_1 = 3t(1-t)^2$
 $B_2 = 3t^2(1-t)$
 $B_3 = t^3$

and $G = \begin{bmatrix} P_{00} & P_{01} & P_{02} & P_{03} \\ P_{10} & P_{11} & P_{12} & P_{13} \\ P_{20} & P_{21} & P_{22} & P_{23} \\ P_{30} & P_{31} & P_{32} & P_{33} \end{bmatrix}$



Tessellated Bezier Patch

References

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- ◉ John Owens, University of California ,GPU Memory Model Overview
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- ◉ Bill Bilodeau, 2010, Direct3D 11 Tutorial: Tessellation
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